Course Type	Course Code	Name of Course	L	Т	Р	Credit
DC	ECC206	Analog Circuits	3	1	0	11

Course Objective

The objective of the course is to develop an ability to analyze and design electronic circuits using discrete components like BJT, MOSFET and to understand the working of important circuits like amplifiers, oscillators, operational amplifiers and their applications.

Learning Outcomes

Upon successful completion of this course, students will:

- acquire a basic knowledge device models and apply them for circuit analysis
- develop the ability to analyze and design analog electronic circuits using discrete components
- be able to design amplifiers and oscillators, current sources etc. based on the performance criteria

Unit			
Unit	Topics to be Covered	Lecture	Learning Outcome
No.		Hours	
1	BJT and MOSFET: Biasing, small signal models (Hybrid-	6+3T	Acquire the techniques for modelling
	π model), input output resistance, frequency response		BJT and MOSFET
2	Single stage Transistor amplifiers: Common base,	7+2T	Develop the understanding about the
	common emitter, common collector, common source,		circuits and performance of different
	common gate, common drain configuration, Multiple		amplifier configurations
	stage amplifiers: CE-CC, CC-CC, Darlington		
	configuration, Cascode configuration		
3	Current Sources, Two transistor current source, Basic	7+2T	Acquire ability to analyze and design
	three transistor current source, Wilson current source,		Various types of current sources
	Widlar current source, Active loads, Multi-transistor		
	current mirror, Cascode Current Source, Different		
	MOSFET based current sources		
4	Feedback: Effect of negative feedback, Feedback	4+1T	Understanding of the general
	configurations (series-shunt, series-series, shunt-shunt,		characteristics of negative-feedback
	shunt-series), loop gain, stability		amplifiers and analyze the
			parameters of various feedback
			configurations
5	Output stages, Class-A, AB, C and their efficiency. Class-	6+2T	Understand the features of different
	B push pull configuration		classes of amplifiers and analyze their
			characteristics
6	Operational Amplifier: Internal circuit (input stage, gain	12+4T	Knowledge of the internal
	stage, level shifter, output stage), Differential amplifier,		configuration of an operational
	MOS and BJT differential pair, Large-Signal operation,		amplifier along with its unique
	Small-Signal operation, Non-ideal characteristics of		features and applications
	differential amplifier, input offset voltage, input bias and		
	offset currents and OPAMP Applications		
	Total	56	

Textbook:

1. Microelectronic Circuits: Theory And Applications, A. S. Sedra, K. C. Smith and A. N. Chandorkar, Oxford University Press (2017)

Reference Books:

- 1. Microelectronics, J. Millman and A. Grabel, McGraw Hill Education (2017)
- 2. Analysis and Design of Analog Integrated Circuits, P. R. Gray, P. J. Hurst, S. H. Lewis, R. G. Meyer, Wiley (2009)
- 3. Microelectronics Circuit Analysis and Design, D. Neamen, McGraw-Hill Education (2009)
- 4. Microelectronics, B. Razavi, Wiley (2018)
- 5. The Art of Electronics, P. Horowitz and W. Hill, Cambridge University Press; 3 edition (2015)