

Electronic Device and Circuits Lab (0-0-3) Subject Code: ECC210

SL No	Name of the Experiments	No of Week
1	Design of common emitter amplifier with proper Q-point setting and study the frequency response of the amplifier	01
2	Design and study of Darlington pair circuit with specified β values and supply voltage	01
3	Design and performance study of an inverting and a non-inverting amplifier using OPAMP for specified gains.	01
4	Study of OPAMP as an adder, subtractor and voltage follower	01
5	Study of OPAMP as integrator and differentiator	01
6	Design and performance analysis of low pass and high pass (1st and 2nd order) filters of specified bandwidth and gain.	01
7	Generation of sine wave from square wave using filter.	01
8	Design and performance analysis of voltage comparator circuit using OP-AMP and design of Zero Crossing Detector (ZCD) using OP-AMP	01
9	Realization of Schmitt triggers circuit using IC 741.	01
10	Design and frequency response analysis of an RC-phase shift oscillator using OP-AMP	01
11	Realization of current mirror circuit	01
12	Performance analysis (frequency response) of cascaded amplifiers.	01
13	Mini project based on Electronics Component	01
14	End Semester Lab Examination and Viva – Voce	01