• •		Course Code	ourse Code Name of Course			L	Т	Р		Credit
		EEC 202 Analog and Digital Electronics					0	0	9	
Cours	se Objectiv	ve					<u>.</u>			
under	standing ap	plication of different	ew about various aspects of An electronic components. The cours rties and frequency response whi	e will also	give an ins	sight in	to the	design	concept	
Learn	ning Outco	omes								
• 1 • 1	understand understand	the operation of well	urse, students will: well as to have an insight of differ known ICs, Transistors, FETs an elopments in the field of sequenti	d their app	lication in	electro	nics.			technology.
Unit No.		Topics to be Covered			Learning Outcome					
1	Stability, thermal ru	of Discrete Devices Transistor heat dissip maway, Junction to ca al Stability, Selection	7	Knowledge about operation and fundamental concepts of Bipolar Junction Transistors						
2	MOSFET MOSFET	fect Transistor – – as an amplifier , Relaxation oscillato on; characteristics;	6	Knowledge about operation and fundamental concepts of Field Effect Transistors, Uni-junction Transistor and their applications						
3	Power am	plifiers –Class, A, B,	4	In depth knowledge about operation of power amplifiers and its frequency response						
4	based cir Voltage f Controlled Comparat amplifier, filters; O	cuits: Inverting amp follower, Summing a d voltage and cu for, Hysteresis and S Log & Anti-log amp	and practical Op-amp, Op-amp blifier, Non-inverting amplifier, amplifier, Differential amplifier, arrent sources, level shifter, Schmitt Trigger. Instrumentation lifiers, Precision rectifier; Active ponostable and astable operation lators.		Understanding the basics of operational amplifier and 555 timer IC; applications of these well- known ICs in electronic circuits					
5	Boolean algebra, logic gates and circuits, Minimization of logic expressions. Karnugh Map; Quine–McCluskey algorithm; Different Logic families RTL, DTL, TTL, ECL, nMOS and CMOS. Waveform generation using gates				Knowledge about basics and fundamental concepts of digital electronics, minimization of Boolean expressions. The students will also gain knowledge about different logic families.					
6	operation Static and circuit re	vices and its chara , transient analysis; d Dynamic CMOS (esponse speed; Pas g logic families. Men		In depth knowledge about MOS devices, working, implementation of logic gates using CMOS, static and dynamic logic gates and their application.						
7	and gate various ty	f combinational circ arrays. Design of S ppes of registers and c – Moore and Mealy		The students will gain knowledge about basic Flip-Flops, Design of combinational and sequential circuit. Special sequential circuits- Moore and Mealy machine						

Text Books

1. Integrated Electronics by J. Millman, C. Halkias C.D. Parikh

2. Digital Logic And Computer Design by M. Morris Mano

Reference Books

1. Electronic Devices and Circuit Theory, R.L. Bollestad, 11th edn., Pearson publication.