

INDIAN INSTITUTE OF TECHNOLOGY (INDIAN SCHOOL OF MINES), DHANBAD

No: 411002/5/2023-NFR

Dated- 13.10.2023

NOTICE

Ref:- Advertisement no: 411002/5/2023-NFR dated 15.04.2023 for recruitment to the post of Junior Technical Superintendent (MIS/Chemistry/Electrical/ Civil/ Mechanical/ Physics/ Electronics/ Computer) (PL-06), IIT (ISM), Dhanbad.

Candidates, shortlisted for recruitment tests to the post of Junior Technical Superintendent (MIS/Chemistry/Electrical/ Civil/ Mechanical/ Physics/ Electronics/ Computer) (PL-06) are hereby informed about the schedule of recruitment test(s) as under:-

Name of the Post- Junior Technical Superintendent				
Name of the Test	Test Date	Reporting Time	Test Time	Venue
Written Test [Objective type]: (100 marks)	27.10.2023 (Friday)	As per Annexure B		New Lecture Hall Complex (NLHC), IIT(ISM) Dhanbad
Trade Test (Computer Proficiency Test): (20 marks)	28.10.2023 (Saturday)			Computer Lab, Fourth Floor, New Lecture Hall Complex (NLHC), IIT(ISM) Dhanbad
Trade Test (Practical Aptitude Test): (80 marks)	28.10.2023 (Saturday)			Respective Departmental Laboratories, IIT (ISM), Dhanbad

The recruitment test of Junior Technical Superintendent shall be conducted in two steps viz. (i) Written Test and (ii) Trade Test. However, Trade Test will consist two parts i.e. Practical Aptitude Test and Computer Proficiency Test.

The candidates, who achieve minimum cut off marks in the Written test, will be shortlisted and called for Trade Test [Practical Aptitude Test (80 marks) + Computer Proficiency Test (20 marks)].

Such shortlisted candidates, who qualify the Trade Test (Practical Aptitude Test + Computer Proficiency Test), will be considered for selection based on the performance in the written test.

The syllabus for Trade Test (Practical Aptitude Test + Computer Proficiency Test) and Written Test for the post of Junior Technical Superintendent is mentioned at **Annexure A**.

Minimum cut off marks:-

No. of Vacancies	No. of candidates to be shortlisted for Trade Test (Practical Aptitude Test + Computer Proficiency Test)	No. of candidates to be shortlisted for Document Verification in order of merit in each category based upon the performance in the Written test subject to qualify the Trade test as under
UR- 09 OBC-06 SC – 04 ST – 02 EWS -02	Candidates scoring minimum qualifying marks as under: UR: Minimum 40% marks in written test OBC/SC/ST/EWS/ PwD: Minimum 35% marks in Written test	Candidates scoring minimum qualifying marks as under: UR: Minimum 40% marks in Trade Test (Practical Aptitude Test + Computer Proficiency Test Separately). OBC/SC/ST/EWS/PwD: Minimum 35% marks in Trade Test (Practical Aptitude Test + Computer Proficiency Test Separately).

Final merit list of the candidates for selection will be prepared, in order of merit, based on their performance in the Written Test as well as successful verification of relevant Documents.

Trade Test (Practical Aptitude Test + Computer Proficiency Test) will have no weightage in the preparation of final merit list except in the tie – breaking condition.

On the basis of marks obtained in Written Test, subject to qualify the Trade Test (Practical Aptitude Test + Computer Proficiency Test), candidates shall be shortlisted for Document Verification.

Tie-breaking Criteria:-

In case two or more candidates secure equal marks in written test against same vacancy under same category in final Merit List following methodology will be adopted for final selection:

- a) Candidate securing more marks in Trade Test (Practical Aptitude Test + Computer Proficiency Test) is to be ranked higher.
- b) In case two or more candidates secure equal marks in Trade Test (Practical Aptitude Test + Computer Proficiency Test), candidate obtaining more marks in Practical Aptitude Test will be ranked higher.
- c) In case two or more candidates secure equal marks in Trade Test (Practical Aptitude Test + Computer Proficiency Test), candidate senior in Age is to be ranked higher.

Document Verification:-

The candidates shortlisted for document verification, on the basis of performance in Written Test, will have to produce the following documents in original (as mentioned in the advertisement) in support of their eligibility:

- (i) Matriculation certificate in support of Date of Birth
- (ii) Mark sheet of educational and technical qualifications
- (iii) Certificate of educational and technical qualifications
- (iv) Experience certificate
- (v) No Objection Certificate from the present employer (if they are employed in Central Govt. / State Govt. / Union Territory Administration/ Central or State Autonomous Bodies/ Central or State PSUs) etc.

In addition, the following documents are also required to be produced for the verification:

For OBC Category: Valid OBC-NCL certificate issued by the Competent Authority in the format prescribed for appointment under Govt. of India.

For EWS Category: Valid EWS certificate issued by the competent authority in the format prescribed for appointment under Govt. of India

For SC/ST Category: Valid caste/Tribe/Category certificate issued by the competent authority in the format prescribed for appointment under Govt. of India.

For PwDs candidates: Valid PwDs certificate issued by the competent authority in the prescribed format.

In case, failing to produce the above documents, their candidature will be cancelled and they will not be selected against the vacancy of the posts, even on passing in Written Test and Trade Test (Practical Aptitude Test + Computer Proficiency Test).

Admit cards to candidates for Junior Technical Superintendent will be available from 17.10.2023. The candidates may visit https://nfr.iitism.ac.in/index.php/recruitment/User_login and download their admit card by login into portal using e-mail Id, Date of Birth and Mobile number (already registered).

Registrar

(I). Syllabus for JTS (MIS)

(A) Trade Test (Practical Aptitude Test): It will be qualifying in nature and will not have any weightage in preparation of final merit list. The syllabus for Practical Aptitude test is as under:-

- HTML and JavaScript for Web Design
- REST API creation using PHP
- MariaDB Administration

B. Trade Test (Computer Proficiency Test):

Word Excel and Power Point

C. Written Test: This will comprise of 100 MCQs based on the Syllabus as under:-

- Client-side scripting: HTML, CSS, JavaScript
- Server-side scripting: PHP
- Persistence Management: MariaDB
- Basics of Computer Science: Operating Systems fundamentals, Linux fundamentals

(II). Syllabus for JTS (Electronics / Mechanical / Electrical / Chemistry & Chemical Biology / Computer Science & Engineering / Physics / Civil)

(A) Trade Test (Practical Aptitude Test)

To be conducted in the laboratories of respective Departments related to their technical qualification.

(B) Trade Test (Computer Proficiency test)

Word, Excel and Power point

(C) Written test

This will comprise of 100 MCQs based on the syllabus of the following for the respective posts:-

- i. JTS (Chemistry) – B.Sc. in Chemistry
- ii. JTS (Electrical) – Diploma in Electrical Engineering.
- iii. JTS (Civil) – Diploma in Civil Engineering
- iv. JTS (Mechanical) – Diploma in Mechanical Engineering
- v. JTS (Physics) – B.Sc. in Physics
- vi. JTS (Electronics) – Diploma in Electronics Engineering
- vii. JTS (Computers) - B.Sc.(IT)/ BCA

Annexure - B**Exam schedule for the post of Junior Technical Superintendent**

Date	Name of Examination	Time Schedule		Venue
27/10/2023 (Friday)	Written Test [Objective type]: (100 marks)	Test time - 09:00 AM - 11:00AM (Reporting Time - 08:30 AM) Junior Technical Superintendent (Physics / Chemistry / Electrical)	Test Time - 12:00 PM - 02:00 PM (Reporting Time - 11:30 AM) Junior Technical Superintendent (Electronics / MIS / Mechanical / Computers)	New Lecture Hall Complex (NLHC), IIT(ISM) Dhanbad
28/10/2023 (Saturday)	Trade Test (Computer Proficiency Test): (20 marks)	All Junior Technical Superintendent 09:00 AM - 09:30 AM (Reporting Time - 08:30 AM)		Computer Lab, Fourth Floor, New Lecture Hall Complex (NLHC), IIT(ISM)
28/10/2023 (Saturday)	Trade Test (Practical Aptitude Test): (80 marks)	Batch wise timing will be notified separately.		Respective Departmental Laboratories, IIT (ISM), Dhanbad
