

भारतीय प्रौद्योगिकी संस्थान (भारतीय खनि विद्यापीठ), धनबाद

Indian Institute of Technology (Indian School of Mines), Dhanbad धनबाद - 826004 (झारखंड), भारत

Dhanbad - 826 004 (Jharkhand), India

423349/2022-ESTT.

Dated: 14th February, 2022

CIRCULAR

Advertisement for Selection in the posts of Technical Superintendent (PL – 7) through Limited Departmental Competitive Examination (LDCE).

IIT(ISM) Dhanbad invites application from regular employees of IIT(ISM) for appointment on following administrative posts through Limited Departmental Competitive Examination (LDCE).

SI.	Name of the	Level in the Pay	Eligibility
No.	post(s)	Matrix of 7 th CPC	
1.	Technical Superintendent	Pay Level – 07	Junior Technical Superintendent(s) of IIT(ISM) Dhanbad having : -
			(a) Three years' Diploma in Engineering or B.Sc. with minimum 55% marks from a recognized institution in a branch/stream relevant to the Academic Departments/Centres of the Institute and having 3 years relevant experience in the revised Pay Level – 6 (Pre-revised Pay Band-2 with Grade Pay of Rs.4200/-) or equivalent in a Govt. Dept./Autonomous Bodies/ Private Sector Organizations of repute.
			(b) Should have experience of working in MS- Office and other computer software relevant to the area of work/specialization.

General Instructions

1. Promotion through LDCE will be done as per the Internal Advertisement, Screening of the received applications and Screening of APARs/ACRs. The candidates will be evaluated by the duly constituted Selection Committee as per the following:-

Step I: Written Test - 80 marks
Step II: Computer Proficiency Test - 20 marks
Total - 100 marks

- 1.1 Selection of the candidates will be on the basis of the marks obtained in the Written Test and Computer Proficiency Test.
- 2. The Selection Committee shall decide the minimum qualifying marks/standards for the aforesaid tests, which shall be final.

- 3. The selection in above posts as advertised will be made as per the provisions mentioned in 'Cadre Structure and Recruitment Rules and Promotion Policy for Non-Teaching Posts' of IIT(ISM) uploaded in the Institute's website and relevant Government rules.
- 4. Candidates falling short or not fulfilling any of the criterion as mentioned in above rules will not be considered for promotion and their application will be summarily rejected.
- 5. Candidates are required to calculate their experience as on cut-off date which is 01/07/2022.
- 6. IIT(ISM) through its competent Authority reserves the right to Amend / Cancel / Withdraw / add / delete any content of the above advertisement.
- 7. Duly filled and signed application with all necessary attachments must be sealed in an envelope and it should reach the Establishment section through Speed post/registered/By Hand by 6 P.M. on or before 21.02.2022. The Sealed envelope is to be addressed to Assistant Registrar (Establishment), Ground floor, Administrative block, IIT(ISM), Dhanbad- 826004, Jharkhand.

REGISTRAR (Actg.)

<u>Application form for Limited Departmental Competitive Examination (LDCE)</u> <u>For the post of Technical Superintendent</u>

Last Date to Apply: 21/02/2022

Name (In BLOCK LETTERS)	
with Employee Code	
, ,	
	D
	Passport Size
	Photo
Father's Name	
Present Post / Designation	
Date of joining in Present Post / Designation	
Experience in present Post as on 01.07.2022	
Present Department / Section	
Pay Level and Basic pay of present post as on 01.07.2022	
Phone/Cell No. & Email ID	
Date of Birth	
Category	
Date of joining at IIT (ISM)	
Date of Last promotion (DPC)	
Date of Last upgradation (ACP/MACP)	
Details of Training/program attended (Use overleaf, if required)	

Educational Qualification: Starting with secondary education (please submit the Documentary evidence even if these are available in Establishment Section. Please attach photo copies of all qualifications)

Examination	Branch/Stream	Name of College /University	Year	Percentage/Grade	Class/ Division

l	hereby	declare	that	all	the	entries	in	this	form	as	well	as	attached	photo	copies	of
d	ocumen	ts are tru	ie to	the	best	of my k	no	wled	ge.							

Date:	
Place:	(Signature of Applicant)