## **List of Publications in Reputed Journal**

First Author	Authors	Title	Journal	Volu me	Issue	Page No.	Year	Impac t Factor	DOI
Rashmi Singh	M Mathirajan	Simulation modeling techniques for managing epidemic outbreak: a review, classification schemes, and meta-analysis	Journal of Simulation	NA	NA	1-20	2022	2.205	10.1080/1 7477778.2 022.20670 12.
Rashmi Singh	M Mathirajan	A New Stage-Wise Control Release Policy for Semiconductor Wafer Fabrication Systems	IEEE Transact ions on Semiconductor Manufacturing	34	1	115- 134	2021	2.874	10.1109/T SM.2020.3 042856
Rashmi Singh	M Mathirajan	Investigation of Different Inputs and A New Release Policy in Proposed Simulation Model for Wafer Fabrication System	Sadhana	44	2	41(1-19)	2019	1.188	https://ww w.ias.ac.in /article/ful ltext/sadh/ 044/02/00 41
Rashmi Singh	M Mathirajan	Experimental Investigation for Performance Assessment of Scheduling Policies in Semiconductor Wafer Fabrication – A Simulation Approach	International Journal of Advanced Manufacturing Technology	99	5-8	1503 - 1520	2018	3.266	https://link .springer.c om/article /10.1007% 2Fs00170- 018-2414- y
Mahama ya Mohanty	<b>Rashmi</b> <b>Singh</b> and Ravi Shankar	Improving the Operational Efficiency of Outbound Retail Logistics Using Clustering of Retailers and Consumers	Journal of Modelling in Management	13	3	646- 674	2018	1.86	https://doi. org/10.11 08/JM2- 12-2016- 0137

## <u>International Conference & Paper Presentation Details:</u>

First Author	Author's	Title	Conference	Year	Venue & Date
Pragya Bajpai	Rashmi Singh	Optimize the Idle Freight for Flat Products Via Rail Network: Case Study in Steel Industry	13th Conference of the Association of Asia Pacific Operations Research Societies (2022 APORS)	2022	Richmonde Hotel, Quezon City, Manila (November 9 - 12, 2022)
Rashmi Singh	M Mathirajan	A Simulation Study for Assessing the Impact of Release Policies, Dispatching Rules and System Factors in Semiconductor Wafer Fabrication	Fourth International Conference on Business Analytics and Intelligence (ICBAI 2016)	2016	Indian Institute of Science, Bangalore, India (19-21, Dec. 2016).
Rashmi Singh	M Mathirajan	Simulation Based Experimental Investigation for Performance Assessment of Scheduling Policies in Wafer Fabrication.	International Symposium on Semiconductor Manufacturing Intelligence (ISMI2015)	2015	KAIST, Daejeon, South Korea (16- 18, Oct. 2015).
Rashmi Singh	M Mathirajan	Simulation Based Cause and Effect analysis of Input Variables in Wafer Fabrication Re-entrant Flow.	Multidisciplinary International Scheduling Conference: Theory & Applications (MISTA 2015)	2015	Prague, Czech Republic (25th – 28th August 2015).
Rashmi Singh	M Mathirajan	A Conceptual Framework for the Performance Assessment of Lot Release Policies	The IEEE International Conference on Industrial Engineering and Engineering Management (IEEM 2014)	2014	Malaysia, pp: 948- 952, (9 <sup>th</sup> to 12 <sup>th</sup> December).

## **National Conference & Paper Presentation Details:**

- Rashmi Singh and M Mathirajan. (2015). A Conceptual Simulation Framework for the Performance
  Assessment of Input Variables in Wafer Fabrication. *Presented in National Conference on Recent Trends*and Developments in Statistics (NCRTDS), 21st 23rd February, 2015, M.D. University, and Rohtak.
- Rashmi Singh and M Mathirajan. (2012). Lot Release Control in Semiconductor Wafer Fabrication. Presented in Conference on Optimization Modelling and Applications (OPTIMA), 29th October-1st November, 2012, Delhi University, New Delhi.