



भारतीय प्रौद्योगिकी संस्थान (भारतीय खनि विद्यापीठ), धनबाद

धनबाद, झारखण्ड, भारत, पिन-826004

(मानव संसाधन विकास मंत्रालय, भारत सरकार के अधीन राष्ट्रीय महत्व का एक संस्थान)

INDIAN INSTITUTE OF TECHNOLOGY (INDIAN SCHOOL OF MINES), DHANBAD

DHANBAD, JHARKHAND, INDIA, PIN-826004

(An Institute of National Importance under Ministry of H.R.D., Govt. of India)

STORES & PURCHASE SECTION Phone:(0326) 2235678 || Email : purchase@iitism.ac.in || Website : www.iitism.ac.in

No. EE/INS/238/18-19

Date: 3 October 2018

NOTICE INVITING TENDER

Subject: Supply & Installation of Circuit Analysis and Designing Software

Indian Institute of Technology (Indian School of Mines), Dhanbad invites quotations for the following to be supplied and delivered in Electrical Engineering Department.

S No	Full Description of items/ store	Qty	Delivery
1	Supply & Installation of Circuit Analysis and Designing Software (Detailed Specification is given in Annexure – I)	01 No. (10 Users)	At the Earliest

Tender Schedule

Particulars	Date & Time
Tender Fees*	Rs. 590.00 (Five Hundred & Ninety Only)
Bid Security or Earnest Money Deposit	Rs. 14,000.00 (Fourteen Thousand Only)
Last date and time for submission of tenders	30.10.2018 at 1:00 P.M.
Date and time of opening of tenders	30.10.2018 at 4.00 P.M.

* For submission of tender cost please see the point no. 06 Terms & Conditions.

Tender document containing following document is available on websites of (<https://eprocure.gov.in> & www.iitism.ac.in), which may kindly be referred.

1. You are requested to quote your lowest rates for the supply of above items in the attached format for Financial Bid (Annexure – II)
2. You may send your representative in the office of the undersigned at the scheduled date and time of opening of tender.
3. Tender should be submitted in sealed cover only superscribed with Enquiry No. and due date at the following address only:

The Assistant Registrar (P&S)

Indian Institute of Technology (Indian School of Mines),

Dhanbad – 826 004 Jharkhand



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Terms & Conditions

- 1) The rates should be quoted for each item separately.
- 2) Conditional offer will not be accepted.
- 3) IIT (ISM) does not issue any Form 'C' or 'D' towards sales tax concessional rate. Hence, full rate of sales tax/VAT, GST applicable should be quoted.
- 4) Since IIT(ISM) Dhanbad is an Indian Institute of Technology and subject item will be used for research, hence benefit of reduced rate of GST should be considered if applicable.
- 5) **Educational discount**, if any, should be clearly mentioned.
- 6) **Earnest Money Deposit (EMD)**: Should be submitted in form of A/C payee demand draft drawn in favor of Registrar, IIT(ISM) and payable at SBI, IIT (ISM) campus branch or any other Bank/ Branch located in Dhanbad and Tender Fees must be paid through SB collect only through the link <https://www.onlinesbi.com/prelogin/collecthome.htm?corpID-272342>, tender cost payments through other modes viz. Cash/DD/NEFT/RTGS etc. shall not be accepted. Online payment receipt should be enclosed with Tender Document.
- 7) You are requested to submit your quotation strictly as per the specifications mentioned in the NIT.
- 8) Your tender must be valid for **minimum 90 days** from the date of opening of tender.
- 9) Please mention warranty/ guarantee in your offer clearly. Material/ equipment to be supplied must have minimum warranty/guarantee of **12 months**.
- 10) Each page in the bid document must be numbered properly and duly signed & sealed by the bidder on every page of the bid.
- 11) **The items/ materials shall be required to be delivered at EE Department/ Section through Purchase & Store Section, IIT (ISM) Dhanbad at the risk and cost of the tenderer.**
- 12) Unloading and installation shall be the complete responsibility of the supplier.
- 13) The stores are required to be delivered within 30 days. Late delivery may not be accepted.
- 14) The items offered should be of good quality confirming to BIS standards, wherever applicable.
- 15) **Advance payment is not admissible**. Payment shall normally be made within 30-45 days subject to receipt and acceptance & installation of the ordered materials/items PBG and other documents.
- 16) Successful bidders will have to submit Performance Security (PBG) in the form of Bank Guarantee as per the Purchase Order before releasing payment.
- 17) In the event date on which the tender is opened for acceptance is declared to be a holiday, the tenders shall be deemed to remain open for acceptance till the next working day.
- 18) Please send your sealed offer by Regd. Post/ Speed Post/ Courier only along with Courier receipt. By hand delivery of the tender will not be accepted. Tender/ quotation will be received during IIT (ISM) working hours only (i.e. Monday to Friday). *Late or delayed tenders shall be summarily rejected.*
- 19) Any other information that you may like to obtain, you are free to contact IIT (ISM) before submission of tender.
- 20) IIT (ISM) reserves the right to accept and/or to reject any/ all tenders without assigning any reason.

Assistant Registrar



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Annexure-I

Technical Specification for Circuit Analysis and Designing Software.

- (i) Capable of analyzing, designing and real time testing of analog, digital, Spice, S-
- (ii) Parameter, VHDL, MCU, mixed electronic, SMPS, RF, communication and optoelectronic circuits.
- (iii) Capable to generate and debug MCU code using the integrated flowchart tool and test micro controller applications in a mixed circuit environment.
- (iv) Feature like live presentation, multimedia parts, advanced circuit diagrams and virtual instrumentations.
- (v) Provisions for test and debug microprocessor & microcontroller based applications.
- (vi) Feature like virtual measurement library: Digital Millimeter, Storage Oscilloscope, Digital Signal Generator, Signal and Network Analyzer, Logic analyzer, Tina Lab Multifunction PC instrument, Real-Time signal Analyzer, Real-Time Logic Analyzer, Arbitrary Function Generator, Real-Time Storage Oscilloscope, Plug-in Experimenter Modules.
- (vii) Feature like component library: All the Passive, Active, Other Components like time controlled switch, voltage controlled switch, AD and DA converter, 74000 logic family, 4000 logic family, FPGA and CPLD libraries, timer, comparators, CMOS Logic ICs, analog control blocks, IGBT, SCR, MOSFET, Bridge Rectifier, different power and connectors, Different energy sources.
- (viii) Feature like Integrated Multilayer PCB layouts design with 3D View, footprints, visualization of physical parts.
- (ix) Support for embedding discrete components within the PCB stack up.
- (x) CAM File – Editor, Gerber file support
- (xi) Features like Multiple output publishing of PCB file in PDF/A, print or web.
- (xii) Features like Post-layout signal integrity, reflection and crosstalk analysis.
- (xiii) 10 Users.
- (xiv) Perpetual Academic License.



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Annexure - II

Format for Financial Bid

NIT No.: EE/INS/238/18-19

Date: 3 October 2018

Bidders Ref: No.

Date:

GSTIN No.:

Subject: Supply & Installation of Circuit Analysis and Designing Software..

Sl. No.	Full Description of Items with (HSN Code/SAC Code)	Qty.	Rate	Amount
		Packing & Forwarding (if any)		
		Total		
		GST		
		Freight (if any)		
		Installation (if any)		
Amount should be in figure as well as word		Grand Total		

Note:

- 1) Price basis should be FOR IIT(ISM) Dhanbad.
- 2) All the details must be provided as per prescribed format only.
- 3) Prices quoted by the bidders should include all local taxes, VAT, service tax, GST, HSN Code, SAC Code, livies, transportation cost and insurance costs etc. if any